# WEST Search History

| Hide Items | Restore | Clear | Cancel |

DATE: Wednesday, December 15, 2004

| Hide? | Set Name | Query | Hit Count |
|---|---|---|---|
| | | *DB=USPT; PLUR=YES; OP=ADJ* | |
| ☐ | L23 | 119 near5 pin | 8 |
| ☐ | L22 | 119.ti,ab. | 58 |
| ☐ | L19 | isolat$ near4 clock | 1000 |
| ☐ | L18 | l17 and l3 | 42 |
| ☐ | L17 | l12 or l13 or l14 or l15 or l16 | 3477 |
| ☐ | L16 | 713/401 | 756 |
| ☐ | L15 | 713/400 | 1537 |
| ☐ | L14 | 713/600 | 1231 |
| ☐ | L13 | 713/501 | 1075 |
| ☐ | L12 | (713/500)[CCLS] | 575 |
| ☐ | L11 | (713/500)![CCLS] | 575 |
| ☐ | L10 | double near2 defined near3 pin | 5 |
| ☐ | L9 | double nesr2 defined near3 pin | 0 |
| ☐ | L8 | L3 near5 (mux or switch or multiplexer) | 35 |
| ☐ | L7 | L3 near10 clock | 27 |
| ☐ | L6 | L3 near10 pin | 9 |
| ☐ | L5 | L3 near10 clock pin | 0 |
| ☐ | L4 | L3 near5 chipset | 4 |
| ☐ | L3 | L2 or l1 | 1783 |
| ☐ | L2 | (various or different) adj type near5 (cpu or processor) | 1345 |
| ☐ | L1 | (first or second) adj type near5 (cpu or processor) | 502 |

END OF SEARCH HISTORY